

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF EXPRESS MAIL UNDER 37 C.F.R. §1.10

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I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above and is addressed to:

Commissioner for Patents Box Patent Application Washington, DC 20231

Derrick Brown

### UTILITY PATENT APPLICATION TRANSMITTAL

(For new non-provisional applications under 37 CFR § 1.53(b))

Continuing Application:  Divisional	of prior application No.: 08/661,027				
Inventor(s) Frank Worrell and Hartvig Ekn	er Filed: June 10, 1996				
Title: Microprocessor Including a Mode	Examiner: S. Najjar				
Detector for Setting Compression Mo	de Group/Art Unit: 2758				
-	Assignee: LSI Logic Corporation				
	Recorded at Reel 8068, Frame 0560				
Power of Attorney in Prior Application: B. Noël Kivlin					
Conley, Rose, & Tayon, P.C.					
P.O. Box 398					
Austin, Texas 78767-0398					

#### **Application Elements**

1. Filing Fee

The filing fee is calculated as shown below.

Total Claims	40	-20=	20	x \$18.00=	\$360.00
Independent Claims	8	-3 =	5	x \$80.00=	\$400.00
Multiple Dependent Claims Fee:					·
	\$710.00				
	\$1,470.00				
	_				
	\$1,470.00				

☐ The Commissioner is hereby authorized to charge the filing fee and any other fees which may be required or credit any overpayment to LSI Logic Corporation, Deposit Account No. 12-2252/5201-02906/P2920-1D/BNK.

2. Specification

51 page(s) of specification; 9 page(s) of claims, 1 page(s) of abstract

3. Drawings

Formal Figure(s) 1-13 on 20 sheet(s)

4. Oath or Declaration

<ul> <li>Newly executed</li> <li>Copy from a prior application (see 37 C.F.R. § 1.63(d))</li> <li>Deletion of Inventor(s) (in continuation or divisional applications):</li> <li>Delete the following inventor(s) named in the prior non-provisional application:</li> </ul>
The inventor(s) to be deleted are set forth on a signed sheet attached hereto.  5. The entire disclosure of the prior application(s) referred to above is/are considered to be part of the accompanying application and is hereby incorporated by reference herein.
6. Microfiche Computer Program (Appendix)
7. Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)
Computer Readable copy
Paper Copy (identical to computer copy)  Statement verifying identity of above copies
8. Assignment Papers
9. Notice of Recordation of Assignment from parent application evidencing assignment
10. Power of Attorney
☐ Is attached.
The power of attorney appears in the original papers of the prior application.
Since the power does not appear in the original papers, a copy of the power in the prior
application is enclosed.
11. Information Disclosure Statement (IDS)  Copies of IDS Citations
12. Amendments
A preliminary amendment is enclosed.
Cancel in this application claim(s) before calculating the filing fee. At least one
independent claim is retained for filing purposes.
Amend the specification by inserting before the first line the sentence: This application is a
divisional application of U.S. patent application serial number 08/661,027, filed June 10, 1996.
13. Return Receipt Postcard
14. Small Entity Status
A small entity statement is enclosed.
A small entity statement was filed in the prior non-provisional application and such status is still proper and desired.
Is no longer claimed.
15. Priority of foreign application number, filed on is claimed under
35 U.S.C. §§ 119(a)-(d)
16. Petition under 37 C.F.R. § 136 for Extension of Time
17. Other: Fee Authorization form
Address all future correspondence to:
David G. Pursel  LSI Logic Corporation
Patent Law Department
1551 McCarthy Blvd., MS D-106
Milpitas, CA 95035
(408) 433-8708
Signature
Signature  Name  B. Noël Kivlin
Registration No. 33,929
Date <u>4-3-01</u>

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Atty. Dkt. No:

5201-02906/P2920-1D

Inventor(s):

Frank Worrell and Hartvig Ekner

Title:

Microprocessor Including a Mode

**Detector for Setting Compression** 

Mode

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Assistant Commissioner for Patents Box Patent Application Washington, DC 20231

Derrick Brown

## FEE AUTHORIZATION

\$\times\$ \times\$ \times\$

Commissioner for Patents Washington, D.C. 20231

The Commissioner is hereby authorized to charge the following fee to LSI Logic Corporation Deposit Account Number 12-2252/5201-02906/P2920-1D/BNK:

Total Claims	40	-20=	20	x \$18.00=	\$360.00
Independent Claims	8	-3 =	5	x \$80.00=	\$400.00
		Basic Fee:	\$710.00		
				Total:	\$1,470.00

Attorney Docket No.:

5201-02906

The Commissioner is also authorized to charge any extension fee or other fees which may be necessary to the same account number.

Signature

Name

Registration No.

Date

B. Noël Kivlin

33,929

4-3-01